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The invention relates to integrated circuits and more particularly to a voltage multiplication circuit for a high power device such as a power switching transistor. High power circuit systems such as power switching circuits, may require a voltage multiplication circuit coupled to the power switching circuit. In some cases the voltage multiplication circuit has the effect of driving or sinking larger voltage currents from the power switching circuit. This is particularly true in bipolar junction transistors and power MOSFETs. It is desirable to be able to scale the voltage multiplication circuit to provide a larger voltage multiplication. It is also desirable to be able to use one type of voltage multiplication component for each voltage multiplication circuit. This is particularly useful when the voltage multiplication circuit is used with a switching circuit where space may be at a premium. It is also desirable to be able to use a single type of voltage multiplication component for any voltage multiplication circuit. When using a wide or multi-voltage band semiconductor power device, it is also advantageous to eliminate the need to wire the device for DC current gain, as well as to eliminate wasted space that would otherwise exist in wire bonding pads for the device. According to the present invention a voltage multiplication circuit and method is provided for a high power device. The circuit and method provides for a multiplicative gain that may be greater than one, or more than two. The circuit uses a voltage multiplier to achieve the desired voltage multiplication. The voltage multiplier preferably operates in a voltage mode as well as a current mode. The power device is preferably coupled to a voltage multiplier circuit. The voltage multiplier circuit preferably may provide for voltage multiplication of the voltage applied to the power device to a voltage greater than the voltage applied to the power device, by a desired amount. In one embodiment of the invention the voltage multiplier circuit preferably further provides for voltage division by a desired division factor greater than one. In one embodiment of the invention the voltage multiplier circuit and the voltage divider circuit are provided within a single integrated circuit. In another embodiment of the invention the voltage multiplier circuit preferably is provided outside of the power device and coupled to the power device by a back-plane connection. In another embodiment of the invention the voltage multiplier circuit provides for a gain greater than one and a ratio between the voltage applied to the power device and the voltage multiplier equal to the desired divisor. The power device may be either a bipolar junction transistor (BJT), M

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